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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/630,123	07/30/2003	Wenkwei Lou	BP2621	6723
	7590 09/17/200 RRISON & MARKISO	EXAMINER		
P.O. BOX 1607		LAMARRE, GUY J		
AUSTIN, TX 78716-0727			ART UNIT	PAPER NUMBER
			2112	
•		•		
			MAIL DATE	DELIVERY MODE
			09/17/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)				
	10/630,123	LOU, WENKWEI				
Office Action Summary	Examiner	Art Unit				
•	Guy J. Lamarre	2112				
The MAILING DATE of this communication app	·					
Period for Reply		•				
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim vill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 30 Ju	<u>ly 2003</u> .					
2a) ☐ This action is FINAL . 2b) ☒ This	This action is FINAL . 2b)⊠ This action is non-final.					
	☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	i3 O.G. 213.				
Disposition of Claims						
4) ⊠ Claim(s) 1-22 is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) □ Claim(s) is/are allowed. 6) □ Claim(s) is/are rejected. 7) □ Claim(s) is/are objected to. 8) ⊠ Claim(s) 1-22 are subject to restriction and/or expressions.	vn from consideration.					
Application Papers						
9)☐ The specification is objected to by the Examine	г.					
	10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
Applicant may not request that any objection to the	- · ·	• •				
Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Ex	• • • • • • • • • • • • • • • • • • • •	• • •				
	aminer. Note the attached Office	Action of form FTO-132.				
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: Certified copies of the priority documents have been received. Certified copies of the priority documents have been received in Application No Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachment(s)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	nte				

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Election/Restriction

* This application contains claims directed to the following patentably distinct species of the claimed invention:

Species I: Independent Claims 1, 9 and intervening claims, drawn to a user input device comprising: a switch matrix having a plurality of plurality of rows and columns; scan logic operable to detect signals corresponding to operation of said rows and columns of said switch matrix and to generate an output signal in response thereto; a test control bus operable to provide test signals to said scan logic; a storage module for storing an executable test sequence program for generating a plurality of signals corresponding to a known operating condition of said switch matrix; a general purpose input-output (GPIO) module operable to provide said a plurality of test signals to said scan logic via said test control bus; and a processor operable to initiate execution of said executable test sequence program and further operable to compare said output signal of said scan logic to a known reference signal to obtain an indication of the operating condition of said scan logic.

Species II: Independent Claims 5, 17 and intervening claims, drawn to a method of testing a key switch matrix on a user input device, comprising: providing a plurality of test signals via a general purpose input-output module (GPIO) to an input of a scan logic module; processing said test signals using said scan logic and generating an output signal therefrom; comparing said output signal to a reference signal corresponding to a known operation condition of said switch matrix; generating an error signal if said output signal differs from said reference signal corresponding to said known operating condition of said switch matrix.

Species III: Independent Claims 13, 21 and intervening claims, drawn to a user input device comprising: a plurality of sensors operable to generate quadrature signals corresponding to operation of said user input device; quadrature signal detection circuitry operable to state changes in said quadrature signals and to generate an output signal in response thereto; a test control bus operable to provide test signals to said quadrature signal detection circuitry; a storage module for storing an executable test sequence program for generating a plurality of signals corresponding to a known operating condition of said quadrature sensors; a general purpose input-output (GPIO) module operable to provide said a plurality of test signals to said quadrature signal detection circuitry via said test control bus; and a processor operable to initiate execution of said executable test sequence program and further operable to compare said output signal of said quadrature signal detection circuitry to a known reference signal to obtain an indication of the operating condition of quadrature detection circuitry.

- * Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is finally held to be allowable.
- * Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.
- * Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which depend from or otherwise require all the limitations of an

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allowable generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species. MPEP § 809.02(a).

Should applicant traverse on the ground that the species are not patentably distinct.

applicant should submit evidence or identify such evidence now of record showing the species to

be obvious variants or clearly admit on the record that this is the case. In either instance, if the

examiner finds one of the inventions unpatentable over the prior art, the evidence or admission

may be used in the rejection under 35 U.S.C. 103(a) of other invention.

Applicant is reminded that upon the cancellation of claims to a non-elected invention, the

inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the

currently named inventors is no longer an inventor of at least one claim remaining in the

application. Any amendment of inventorship must be accompanied by a request under 37 CFR

1.48(b) and by the fee required under 37 CFR 1.17(i).

Conclusion

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks, Washington, D.C. 20231

or faxed to: (571) 273-8300 for all formal communications.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Guy J. Lamarre, P.E., whose telephone number is (571) 272-

3826. The examiner can normally be reached on Monday to Friday from 9:30 AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jacques Louis-Jacques, can be reached at (571) 272-6962.

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Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the Group receptionist whose telephone number is (571) 272-3609.

Information regarding the status of an application may also be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Guy J. Lamarre, P.E.

Primary Examiner